

AMD Opteron™ 6200 Series processors

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Compiler Options Quick Reference Guide

Open64

Latest release: 4.5.1, December 2011

<http://developer.amd.com/open64>

Architecture	
Generate instructions specific to Interlagos	<code>-march=bdver1</code>
Generate instructions for the local machine	<code>-march=auto</code>
Optimization Levels	
Disable all optimizations	<code>-O0</code>
Local optimizations	<code>-O1</code>
Global optimizations (default)	<code>-O2</code>
Additional aggressive optimizations	<code>-O3</code>
Maximize performance	<code>-Ofast</code>
Additional Optimizations	
Autoparallelization	<code>-apo</code>
Feedback directed optimization	<code>-fb-create</code> <code>-fb-opt</code>
Huge pages	<code>-HP</code>
Interprocedural Analysis and Optimizations	<code>-ipa</code>
Link to ACML 5.0	<code>-L/opt/acml5.0.0/ open64_64_fma4/ lib -lacml</code>
Loop nest optimizations, vectorization, prefetch, fission, fusion	<code>-LNO:fission=n</code> <code>-LNO:fusion=n</code>
Multicore scalability	<code>-mso</code>
OpenMP	<code>-mp</code>
Prefetch (disabled by default)	<code>-LNO:prefetch</code> <code>-LNO:prefetch_ahed</code>
Floating point accuracy	
Floating point accuracy	<code>-fp-accuracy</code>

gcc

Latest release: 4.7, March 2012

<http://gcc.gnu.org>

Architecture	
Generate instructions specific to Interlagos	<code>-march=bdver1</code>
Optimization Levels	
Disable all optimizations (default)	<code>-O0</code>
Local optimizations	<code>-O1</code>
Global optimizations	<code>-O2</code>
Additional aggressive optimizations	<code>-O3</code>
Maximize performance	<code>-Ofast</code>
Additional Optimizations	
AVX tuning	<code>-mprefer-avx128</code>
Enable unrolling	<code>-funroll-all-loops</code>
Generate prefetch instructions for loops	<code>-fprefetch-loop-arrays</code> <code>--param prefetch-latency=300 (300-700)</code>
Inline string operations	<code>-minline-all-stringops</code>
Link to ACML 5.0	<code>-L/opt/acml5.0.0/ gfortran64_fma4/ lib -lacml</code>
OpenMP	<code>-fopenmp</code>
Profile guided optimization	<code>-fprofile-generate</code> <code>-fprofile-use</code>
Turn off partial redundancy elimination	<code>-fno-tree-pre</code>
Vectorization	<code>-ftree-vectorize</code>
Floating point accuracy	
Enable generation of code that follows IEEE arithmetic	<code>-mieee-fp</code>
Enable faster, less precise math operations	<code>-ffast-math</code>

For more information, visit <http://developer.amd.com>

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ICC

Latest release: 12.1, January 2012

<http://software.intel.com>

Architecture	
Generate instructions that run on Interlagos	-msse3 (avoid -mAVX)
Optimization Levels	
Disable all optimizations	-O0
Speed optimization without code growth	-O1
Enable optimization including vectorization	-O2
Aggressive optimization	-O3
Maximize performance	-fast
Additional Optimizations	
Aggressive unrolling	-unroll-aggressive
Disable improved precision floating divides	-no-prec-div
Enable vectorization	-simd
Interprocedural Optimization	-ipo
Link to ACML to 5.0	-L/opt/acml5.0.0/ifort64_fma4/lib -lacml
OpenMP	-openmp
Prefetch optimization	-opt-prefetch
Profile generated optimization	-prof-gen -prof-use
Use optimized header definitions	-use-intel-optimized-headers
Floating point accuracy	
Floating point accuracy	-fp-model

PGI

Latest release: 12.3, March 2012

<http://www.pgroup.com>

Architecture	
Generate instructions that run on Interlagos	-tp bulldozer
Optimization Levels	
Disable all optimizations	-O0
Local optimization	-O1
Global optimization	-O2
Aggressive global optimization	-O3
Hoist guarded invariant floating point expressions	-O4
Maximize performance	-fast
Additional Optimizations	
Huge pages	-Msmartalloc=huge
Autoparallelize loops	-Mconcur
Enable vectorization	-Mvect
Interprocedural Optimization	-Mipa=fast,inline
Link to ACML to 5.0	-L/opt/acml5.0.0/pgi64_fma4/lib -lacml
OpenMP	-mp
Prefetch instructions	-Mvect=prefetch
Profile guided optimization	-Mphi -Mpfo
Unroll loops	-Munroll
Floating point accuracy	
Generate relaxed precision code	-Mfprelaxed
Perform floating point operations in conformance with IEEE standard	-Kieee

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